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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of: G. DIWAKAR VISHAKHADATTA ET AL.

Filed: FEBRUARY 26, 2002

For: APPARATUS AND METHODS FOR CALIBRATING SIGNAL-  
PROCESSING CIRCUITRY

Serial No.: 10/083,633

Group Art Unit: 2682

Examiner: TRAN, TUAN

Atty Dkt: SILA:080

Pursuant to 37 C.F.R. 1.8, I certify that this correspondence is being deposited with the U.S. Postal Service in a first class, postage prepaid envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date below:

*10/01/05*  
Date

*Mandy Jones*  
Name

ATTN: OFFICIAL DRAFTSMAN  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**SUBMISSION OF FORMAL DRAWINGS**

Applicants hereby submit the formal drawings for the above-referenced application and request that these drawings be accepted for filing.

Respectfully submitted,

*Maximilian R. Peterson*

Maximilian R. Peterson  
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